



BCT2186L 1A, Low Dropout, Linear Regulators

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GENERAL DESCRIPTION

The BCT2186L series low-power, low-noise, low-dropout, CMOS linear voltage regulators operate from a 1.6V to 5.5V input voltage. They are the perfect choice for low voltage, low power applications. A low ground current makes this part attractive for battery operated power systems. The BCT2186L series also offer ultra-low dropout voltage to prolong battery life in portable electronics. Output current minimum limit is 1A, and over-current protection limit is set at 1.4A typical.

These devices feature a shutdown function and are offered in active low with auto discharge. The output voltage is adjustable in the range of 0.6V to 5.0V. Other features include a low shutdown current, fold-back current limit and thermal shutdown protection.

The BCT2186L is available in Green DFN2x2-6L packages. It operates over an ambient temperature range of -40°C to +85°C.

FEATURES

- Wide Input Voltage Range: 1.6V to 5.5V
- Adjustable Output from 0.6V to 5.0V
- 1A Current Rating
- Low Dropout Voltage
- Output Current Limit
- Quick output discharge
- Thermal-Overload Protection
- High PSRR(75dB at 1kHz)
- -40°C to 85°C Operating Temperature Range
- Available in Green DFN2x2-6L Packages

APPLICATIONS

Cellular Telephones
Cordless Telephones
PCMCIA Cards
Modems
MP3 Player
Hand-Held Instruments
Portable/Battery-Powered Equipment

ORDERING INFORMATION

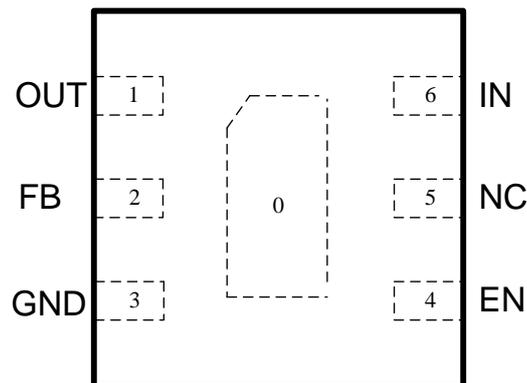
Order Number	V _{OUT} (V)	Package Type	Temperature Range	Marking	QTY/Reel
BCT2186LELTAJ-TR	ADJ	DFN2x2-6L	-40°C to +85°C	2186 XXXX	3000

Note:

"XXXX" in Marking will be appeared as the batch code.

PIN CONFIGURATION

**DFN2x2-6L
(Top View)**



PIN DESCRIPTION

NO.	NAME	FUNCTION
1	OUT	Regulator Output.
2	FB	Adjustable Voltage Version Only. This is used to set the output voltage of the device.
3	GND	Ground
4	EN	Enable pin, active high.
5	NC	No Connect.
6	IN	Regulator Input.
0	GND	Ground, Thermal pin.



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ABSOLUTE MAXIMUM RATINGS

IN to GND.....	-0.3V to 6.5V
EN to GND.....	-0.3V to V_{IN}
OUT ,FB to GND.....	-0.3V to ($V_{IN}+0.3V$)
Output Short-Circuit Duration.....	Infinite
Power Dissipation, $P_D@T_A=25^\circ C$	
DFN2x2-6L.....	0.9W
Package Thermal Resistance	
DFN2x2-6L, θ_{JA}	140°C/W
Junction Temperature.....	150°C
Operating Temperature Range.....	-40°C to +85°C
Storage Temperature Range.....	-65°C to 150°C
Lead Temperature (Soldering, 10 sec).....	260°C
ESD Susceptibility HBM.....	4000V

NOTE:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. Broadchip recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

Broadchip reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact Broadchip sales office to get the latest datasheet.

ELECTRICAL CHARACTERISTICS

($V_{IN} = V_{OUT(NOMINAL)} + 0.5V^{(1)}$, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, unless otherwise specified.)

PARAMETER	SYM	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage	V_{IN}		1.6		5.5	V
Output Voltage Accuracy ⁽¹⁾		$I_{OUT}=1mA$	-1.0		1.0	%
Feedback Voltage	V_{FB}	$I_{OUT} = 1mA$	0.594	0.6	0.606	V
Maximum Output Current ⁽¹⁾		$V_{IN}=1.6V$ or $(V_{OUT}+0.5V)$	1			A
Current Limit ⁽¹⁾	I_{LIM}	$V_{IN}=1.6V$ or $(V_{OUT}+0.5V)$		1.4		A
Short-Circuit Current ⁽¹⁾	I_{SHORT}	$V_{IN}=1.6V$ or $(V_{OUT}+0.5V)$		0.6		A
Ground Pin Current	I_Q	No load, $EN=2V$		30		μA
Dropout Voltage ⁽²⁾		$I_{OUT}=1A$	$V_{OUT} = 0.6V$		0.94	V
			$V_{OUT} = 0.8V$		0.78	
			$V_{OUT} = 1.2V$		0.47	
			$V_{OUT} = 1.5V$		0.30	
			$V_{OUT} = 1.8V$		0.22	
			$V_{OUT} = 2.5V$		0.15	
			$V_{OUT} = 2.8V$		0.14	
			$V_{OUT} = 3.3V$		0.13	
			$V_{OUT} = 3.6V$		0.12	
			$V_{OUT} = 5V$		0.11	
Line Regulation ⁽¹⁾	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	$V_{IN}=1.6V$ or $(V_{OUT}+0.5V)$ to $5.5V$, $I_{OUT}=1mA$		0.05		%/V
Load Regulation	$\frac{\Delta V_{OUT}}{\Delta I_{OUT} \times V_{OUT}}$	$I_{OUT}=1mA$ to $1A$, $C_{OUT}=1\mu F$, $V_{OUT}>2V$		0.002		%mA
		$I_{OUT}=1mA$ to $1A$, $C_{OUT}=1\mu F$, V_{OUT} $\leq 2V$		0.004		
Power Supply Rejection Ratio	PSRR	$I_{LOAD}=50mA$, $C_{OUT}=1\mu F$, $V_{IN}=V_{OUT}+1V$	$f=1kHz$	75		dB

ELECTRICAL CHARACTERISTICS

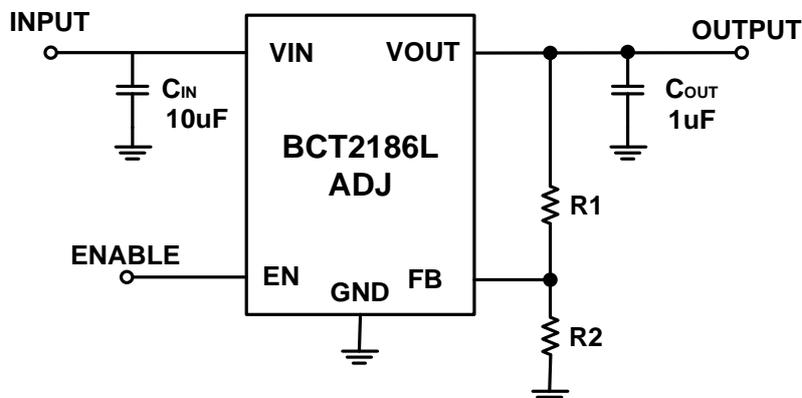
($V_{IN} = V_{OUT(NOMINAL)} + 0.5V^{(1)}$, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, unless otherwise specified.)

PARAMETER	SYM	CONDITIONS	MIN	TYP	MAX	UNITS
SHUTDWN⁽³⁾						
EN Input Threshold	V_{IH}	$V_{IN}=1.6V$ to $5.5V$,	1.5			V
	V_{IL}	$V_{EN}=-0.3V$ to V_{IN}			0.3	
EN Input Bias Current	I_{IN}	EN=5.5V		1.5		uA
		EN=0V		0.01		uA
Shutdown Supply Current	$I_{Q(SHDN)}$	EN=0.4V		0.01	1	uA
Shutdown Exit Delay ⁽⁴⁾		$C_{OUT}=1\mu F$, No Load		30		us
THERMAL PROTECTION						
Thermal Shutdown Temperature	T_{SHDN}			150		$^{\circ}C$
Thermal Shutdown Hysteresis	ΔT_{SHDN}			15		$^{\circ}C$

NOTES:

- $V_{IN} = V_{OUT (NOMINAL)} + 0.5V$ or $1.6V$, whichever is greater.
- The dropout voltage is defined as $V_{IN} - V_{OUT}$, when V_{OUT} is 100mV below the value of nominal V_{OUT} .
- $V_{EN} = -0.3V$ to V_{IN}
- Time needed for V_{OUT} to reach 90% of final value.

TYPICAL APPLICATION CIRCUIT



NOTE: $V_{OUT} = (R1 + R2) / R2 \times 0.6 V$

APPLICATION NOTE

Programming the BCT2186L Adjustable LDO regulator

The output voltage of the BCT2186L adjustable regulator is programmed using an external resistor divider as show in Figure as below. The output voltage is calculated using equation as below:

$$V_{OUT} = V_{FB} \times \left(1 + \frac{R1}{R2}\right)$$

Where:

$$V_{FB}=0.6V$$

Resistors R1 and R2 should be chosen for approximately 50uA divider current. Lower value resistors can be used for improved noise performance, but the solution consumes more power. Higher resistor values should be avoided as leakage current into/out of FB across R1/R2 creates an offset voltage that artificially increases/decreases the feedback voltage and thus erroneously decrease/increases V_{OUT} .

Enable Function

The BCT2186L features an LDO regulator enable/disable function. To assure the LDO regulator will switch on; the EN turn on control level must be greater than 1.5 volts. The LDO regulator will go into the shutdown mode when the voltage on the EN pin falls below 0.3 volts. For to protect the system, the BCT2186L have a quick discharge function. If the enable function is not needed in a specific application, it may be tied to VIN to keep the LDO regulator in a continuously on state.

Thermal Considerations

Thermal protection limits power dissipation in BCT2186L. When the operation junction temperature exceeds 150°C, the OTP circuit starts the thermal shutdown function turn the pass element off. The pass element turns on again after the junction temperature cools by 15°C.

For continue operation, do not exceed absolute maximum operation junction temperature 150°C. The power dissipation definition in device is:

$$P_D = (V_{IN} - V_{OUT}) \times I_{OUT} + V_{IN} \times I_Q$$

The maximum power dissipation depends on the thermal resistance of IC package, PCB layout, the rate of surroundings airflow and temperature difference between junction to ambient. The maximum power dissipation can be calculated by following formula:

$$P_D(\text{MAX}) = (T_J(\text{MAX}) - T_A) / \theta_{JA}$$



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Where $T_J(\text{MAX})$ is the maximum operation junction temperature 150°C , T_A is the ambient temperature and the θ_{JA} is the junction to ambient thermal resistance. For recommended operating conditions specification of BCT2186L, where $T_J(\text{MAX})$ is the maximum junction temperature of the die (150°C) and T_A is the maximum ambient temperature. The junction to ambient thermal resistance (θ_{JA} is layout dependent) for DFN2x2-6L package is 140°C/W , on standard JEDEC 51-3 thermal test board. The maximum power dissipation at $T_A = 25^\circ\text{C}$ can be calculated by following formula:

$$P_D(\text{MAX}) = (150^\circ\text{C} - 25^\circ\text{C}) / 140 = 893\text{W (DFN2x2-6L)}$$

The maximum power dissipation depends on operating ambient temperature for fixed $T_J(\text{MAX})$ and thermal resistance θ_{JA} . It is also useful to calculate the junction of temperature of the BCT2186L under a set of specific conditions. In this example let the Input voltage $V_{IN} = 3.3\text{V}$, the output current $I_o = 500\text{mA}$ and the case temperature $T_A = 40^\circ\text{C}$ measured by a thermal couple during operation. The power dissipation for the $V_o = 2.8\text{V}$ version of the BCT2186L can be calculated as:

$$P_D = (3.3\text{V} - 2.8\text{V}) \times 500\text{mA} + 3.3\text{V} \times 30\mu\text{A} \\ = 250.01\text{mW}$$

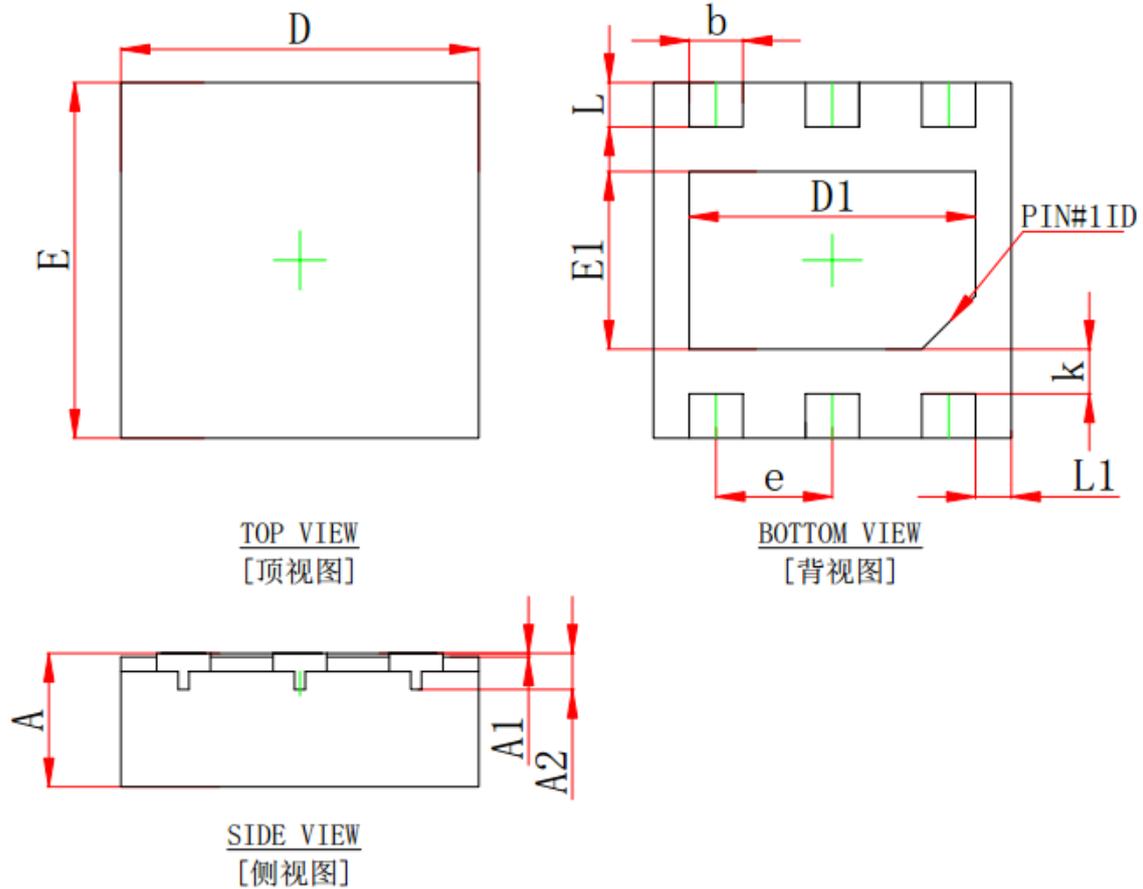
And the junction temperature, T_J , can be calculated as follows:

$$T_J = T_A + P_D \times \theta_{JA} = 40^\circ\text{C} + 0.25\text{W} \times 140^\circ\text{C/W} \\ = 40^\circ\text{C} + 35^\circ\text{C} = 75^\circ\text{C} < T_J(\text{MAX}) = 150^\circ\text{C}$$

For this operating condition, T_J is lower than the absolute maximum operating junction temperature, 150°C , so it is safe to use the BCT2186L in this configuration.

PACKAGE OUTLINE DIMENSIONS

DFN2x2-6L



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Nor.	Max.	Min.	Nor.	Max.
A	0.700	0.750	0.800	0.028	0.030	0.031
A1	0.000	—	0.050	0.000	—	0.002
A2	0.203REF.			0.008REF.		
D	1.924	2.000	2.076	0.076	0.079	0.082
E	1.924	2.000	2.076	0.076	0.079	0.082
D1	1.550	1.600	1.650	0.061	0.063	0.065
E1	0.950	1.000	1.050	0.037	0.039	0.041
b	0.250	0.300	0.350	0.010	0.012	0.014
e	0.650BSC.			0.026BSC.		
k	0.200	0.250	0.300	0.008	0.010	0.012
L	0.200	0.250	0.300	0.008	0.010	0.012
L1	0.200BSC.			0.079BSC.		

PCB Layout Pattern: (Unit: mm)

DFN2x2-6L

